Inventor: Zhongze Wang

Title:

Methods of Forming Transistor Devices

Assignee:

Micron Technology, Inc.

Serial No.: 09/881,407

Filed:

June 13, 2001

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the reference listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted.

Date: 12/18/17/

Attorney:

David Gi Latwesen, Ph.D.

Reg. No. 38,533

Wells, St. John, Roberts, Gregory & Matkin, P.S.

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Micron Technology, Inc.

April 16, 2004

The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the following papers relating to the Patent application of Zhongze Wang, Serial No. 09/881,407, filed 6/13/2001, entitled "Methods of Forming Transistor Devices".

1. PTO Return Receipt Postcard

16191 U.S. PTO

2. Check no. 142522 for \$420.00 (two-monthsextension) 3. Transmittal Form (PTO/SB/21)

4. Fee Transmittal (PTO/SB/17) (in duplicate)

5 Response to November 18, 2003/20/fixe Naction

6. Petition for Extension of Time Under 27 CFR 1.136(a)
7. Copy of SIDS aubmazed 12/.0/01 a/copy of Form PT0-1449

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